OIP & Docket No.: 4425-167

**PATENT** 

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Application of

Hsin-Yu KUO, et al.

Serial No. 09/916,270

Group Art Unit: 2876

Filed: July 30, 2001

Examiner: Unknown

For: SYSTEM AND METHOD FOR INTERLOCKING MANAGEMENT IN

SEMICONDUCTOR MATERIAL SUPPLY EQUIPMENT

## REQUEST FOR CORRECTED FILING RECEIPT

Honorable Commissioner of Patents and Trademarks Washington, DC 20231

Sir:

Attached is a copy of the Filing Receipt received from the U.S. Patent and Trademark Office in the above-referenced application, on which we noticed that the last inventor's address is listed erroneously. Also attached is a copy of the Declaration listing the correct inventors' addresses, which should appear as: **TAIWAN**, **R.O.C.** It is requested that a corrected filing receipt be issued *immediately*.

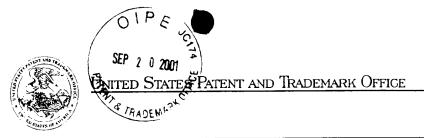
Very truly yours,

LOWE HAUPTMAN GILMAN & BERNER, LLP

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**September 20, 2001** 



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**CONFIRMATION NO. 8682** 

**FILING RECEIPT** 

\*OC00000006524346\*

LOWE HAUPTMAN GILMAN & BERNER, LLP Suite 310 1700 Diagonal Road Alexandria, VA 22314

552 1 0 20<mark>01</mark>

Date Mailed: 09/07/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

### Applicant(s)

Hsin-Yu Kuo, Chia-I, TAIWAN; Dah-Woei Lee, Tainan, TAIWAN; Fu-Chin Yeh, Hsin-Chu, TAIWAN; Yueh-Feng Wu, Taichung, TAIWAN; Chung-Sheng Pang, Hsin-Chu, JAPAN;

### **Assignment For Published Patent Application**

United Microelectronics Corporation;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted 09/06/2001

Projected Publication Date: 01/30/2003

Non-Publication Request: No

Early Publication Request: No

Title

System and method for interlocking management in semiconductor material supply equipment

**Preliminary Class** 

235

Data entry by : ALHAIDARI, KHAIRIA Team : OIPE

Date: 09/07/2001

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